## EXAMINER'S AMENDMENT

1.     An examiner's amendment to the record appears below. Should the changes and/or

additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR

1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the

payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with

Doug Owens on 7/2/08.

The application has been amended as follows:  claims 5, 6, 15 and 16 are canceled.

Claims 7-9 are amended to depend from claim 1.  Claims 17-19 are amended to depend from

claim 11.

Claim 1 is amended to read as follows:

1. (Currently Amended) A memory device for a mobile phone comprising:

a flash memory for storing program data and user data;

an interface circuit for copying program data stored in the flash memory according to

whether data stored in the flash memory is valid;

a first Random Access Memory (RAM) for providing an operation area to store and

execute the copied program data; and

a second RAM for storing data generated during the execution of program data,

wherein the first and second RAMs are independent memories,

wherein the interface circuit comprises a first logic gate for generating a NAND CE (chip

enable) signal, said NAND CE signal enabling the flash memory, and

wherein the interface circuit further comprises a second logic gate for generating a CLE (command latch enable) signal, said CLE signal informing the flash memory that incoming data is a command.

Claim 11 is amended to read as follows:

11. (Currently Amended) A mobile communication device comprising:

an analog circuit for air interfacing the mobile communication device;

a user interface circuit for interfacing between the mobile communication device and a user, wherein the interface circuit is an application-specific integrated circuit (ASIC) including a read-only memory (ROM) for storing program codes and an error correction circuit;

a microprocessor (MPU) for providing overall control of the operation of the mobile device; and

a memory device including

a flash memory for storing program data and user data;

an interface circuit for interfacing the flash memory to the microprocessor, wherein the interface circuit is an application-specific integrated circuit (ASIC) including a read-only memory (ROM) for storing program codes and an error correction circuit;

a first memory for copying the program data of the flash memory; and

a second memory for executing the program data of the first memory,

wherein the first and second memories are independent memories,

wherein the interface circuit comprises a first logic gate for generating a NAND CE (chip enable) signal, said NAND CE signal enabling the flash memory, and

wherein the interface circuit further comprises a second logic gate for generating a CLE

(command latch enable) signal, said CLE signal informing the flash memory that incoming data

is a command.


2.      The following changes to the drawings have been approved by the examiner and agreed

upon by applicant: new formal drawings will be filed because the drawings filed on 12/13/01 and

12/15/04 are informal.  In order to avoid abandonment of the application, applicant must make

these above agreed upon drawing changes.


**END OF EXAMINER'S AMENDMENT**



                                    *Allowable Subject Matter*

3.      Claims 1, 2, 4, 7-13 and 17-20 are allowed.

4.      The following is an examiner's statement of reasons for allowance: the prior art fails to

disclose the recited combinations of elements in a memory device for a mobile phone or a mobile

communication device, including an interface circuit that comprises a first logic gate for

generating a NAND CE (chip enable) signal, said NAND CE signal enabling a flash memory,

and an interface circuit that further comprises a second logic gate for generating a CLE

(command latch enable) signal, said CLE signal informing the flash memory that incoming data

is a command, as set forth in claims 1 and 11; the remaining claims depend from claims 1 and

11.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sam Bhattacharya whose telephone number is (571)272-7917. The examiner can normally be reached on Weekdays, 9-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, George Eng can be reached on (571) 272-7495. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

sb

/Sam  Bhattacharya/
Examiner, Art Unit 2617